Curriculum Vitae

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Gilles Muller
Senior Research Scientist
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Education

Habilitation à diriger des recherches, October 1997, University of Rennes 1
Title : Contribution to the Design of Adaptive and Extensible Operating Systems:
             from Micro-kernel to Partial Evaluation
Jury members : Jean-Pierre Banâtre, Roland Balter, David Powell, Calton Pu,
             Michel Banâtre, Charles Consel.

PhD in computer science, June 1988, University of Rennes 1
Title: The Design and Building of a Reliable Multiprocessor Architecture
Jury members: Laurent Kott, Jacques Lenfant, Gérard Roucairol,
             Jean-Pierre Verjus, Jean-Pierre Banâtre, Michel Banâtre.

Professional Experience

May 2014 - Head of the WHISPER Inria Paris team.

September 2009 - Senior Research Scientist, INRIA,
         member of the REGAL Inria Paris-Rocquencourt team.

March 2002 - August 2009 Professor, Ecole des Mines de Nantes,
         member of the OBASCO EMN/INRIA, LINA team.

October 1988 - February 2002 Associate Research Scientist (*Chargé de recherche*), INRIA
         1996- February 2002 member of the COMPOSE group (created with Charles Consel).
         1988-1996 member of the LSP/SOLIDOR group.

1985-1988: PhD student.

Grants and Contracts

- **ANR VeriAMOS**, 2019-2022, *Verified Abstract Machines for Operating Systems*, Co-Principal Investigator. 121 000 euros.

- **CIFRE Orange**, 2019-2021, Co-Principal Investigator. 30 000 euros.

- **ANR PRCI ITrans**, 2017-2020, *Automatic inference of software transformation rules for automatically back and forward porting legacy infrastructure software*, Participant. 287 820 euros.

- **CRE Orange Lab**, 2016-2017, *Priorisation des performances d'exécution de fonctions réseaux virtuelles dans des infrastructures NFVIs*, Co-Principal Investigator. 60 000 euros.

- **CIFRE Thales**, 2016-2018, *Contrôle prédictible et efficace de processeurs multi-coeurs COTS pour des utilisations temps-réel dur*, Co-Principal Investigator. 45 000 euros.

- **DGA**, 2014-2016, *Fast Driver Development*, Co-Principal Investigator. 155 480 euros.

- **CIFRE Renault**, 2012-2014, *Hierarchical scheduling for virtualized embedded systems*, Co-Principal Investigator. 45 000 euros.

- **CIFRE Orange**, 2012-2014, *Software robustness in OSGi*, Co-Principal Investigator. 30 000 euros.

- **ANR INFRA**, 2012-2015, *INFRA-JVM*, Participant. 202 600 euros.

- **ANR non thématique ABL**, 2009-2012, *A Bug's life*, Principal Investigator. 237 800 euros.

- **ANR logicielle SADAJ**, 2006–2009, *Safe And Deterministic Automotive Java on cost effective microcontrollers*, Co-Principal Investigator. 130 000 euros.

- **ANR non thématique FLFS**, 2006–2009, *Familly of Languages for Familly of Systems*, Participant. 206 000 euros.

- **ARC INRIA VeriTLA+**, 2006–2007, Participant. 47 000 euros global to the project.

- **ANR non thématique Coccinelle**, 2005-2008, *A Language-Based Approach to Managing the Collateral Evolution of Linux Device Drivers*, Principal Investigator. 220 000 euros.

- **CIFRE Jaluna**, 2004-2007, *Scheduling and hierarchies of OSes*, Principal Investigator. 36 000 euros.

- **ACI CORSS**, 2003-2006, *Property verification in system software*, Principal Investigator. 21 000 euros.

- **Microsoft**, 2003, *Port of Bossa under XP*, Principal Investigator. Dollar 25 000.

- **Microsoft**, 2003, *Methodologies for OS development*, Principal Investigator. Euros 20 000.

- **Action Spécifique, CNRS**, 2002, *Methodologies and tools for OS development*, Project manager.

- **ITEA European project**, 1999-2001, *ESAPS - Technologie de développement de lignes de produits*, Co-Principal Investigator (with Charles Consel). FF 526 000.

- **ITEA European project**, 1999-2001, *DESS - Méthodologie de développement de logiciels adaptée aux systèmes embarqués*, Co-Principal Investigator (with Charles Consel). FF 530 000.

- **France Télécom/CNET** (CTI), 1999-2002, *Adaptation de systèmes réflexifs au moyen de langages dédiés*, Principal Investigator. FF 990 000.

- **RNRT**, projet Phénix, 2000-2003, *Noyaux d'infrastructure répartie adaptable*, Principal Investigator. FF 1 089 000.

- **BULL**, 1998-2001, *Optimisation de Java pour les cartes à microprocesseur*, Co-Principal Investigator (with Charles Consel). FF 830 000.

- **Alcatel**, 1997-2000, *Approche déclarative à l'optimisation d'applications de télécommunication - projet Reutel 2000*, Co-Principal Investigator (with Charles Consel). FF 864 000.

- **Thomson Multimédia**, 1999-2000, *Conception d'environnement de programmation pour les langages dédiés au domaine des systèmes embarqués multimédia grand public*, Co-Principal Investigator (with Charles Consel). FF 800 000.

- **Thomson Multimédia**, 1998 (6 month), *Génération automatique de composants logiciels à partir de descriptions de haut niveau*, Co-Principal Investigator (with Charles Consel). FF 250 000.

- **France Télécom/CNET** (CTI), 1995-1998, *Adaptation dynamique de programmes système par spécialisation incrémentale*, Co-Principal Investigator (with Charles Consel). FF 990 000.

- **Conseil Régional de Bretagne**, 1997-1998, *Optimisation de Java pour les services multimédia*, Co-Principal Investigator (with Charles Consel). FF 500 000.

- **Conseil Régional de Bretagne**, 1994-1995, *Étude et mise en œuvre d'une plate-forme ATM locale*, Technical supervision, Principal Investigator Michel Banâtre. FF 660 000.

- **DRET**, 1990-1994, *Étude et réalisation d'une architecture multiprocesseur tolérante aux pannes et du système opératoire associé*, Technical supervision, Principal Investigators Jean-Pierre Banâtre and Michel Banâtre. FF 1 600 000.

- **MRT**, 1989-1992, *Conception et réalisation de cartes prototypes "Mémoire stable"*, Technical supervision, Principal Investigators Jean-Pierre Banâtre and Michel Banâtre. FF 660 000.

- **DRET**, 1987-1988, *Mécanismes logiciels de reprise dans une architecture tolérante aux pannes*, Technical supervision, Principal Investigator Jean-Pierre Banâtre. FF 80 000.

PhD Thesis Supervised

- **Redha Gouicem**, *Gestion coordonnée de ressources CPU et mémoire sur architectures multicoeurs*, Sorbonne University, October 2020, co-supervised with Julien Sopéna. Accessit au prix de thèse 2021 du GDR Réseaux et Systèmes Distribués (RSD).

- **Cédric Courtaud**, *Caractérisation de la sensibilité aux interférences mémoire dans les systèmes temps-réels embarqués sur des plateformes multi-coeurs*, Sorbonne University, January 2020, co-supervised with Julien Sopéna. Cifre with Thales TRT.

- **Antoine Blin**, *Vers une utilisation efficace des processeurs multi-coeurs dans des systèmes embarqués à criticités multiples*, University of Paris 6, January 2017, co-supervised with Julien Sopéna. Cifre with Renault.

- **Florian David**, *Continuous and Efficient Lock Profiling for Java on Multicore Architectures*, University of Paris 6, July 2015, co-supervised with Gaël Thomas.

- **Lisong Guo**, *Boost the Reliability of the Linux Kernel – Debugging Kernel Oopses*, University of Paris 6, December 2014, co-supervised with Julia Lawall.

- **Koutheir Attouchi**, *Managing Resource Sharing Conflicts in an Open Embedded Software Environment*, University of Paris 6, July 2014, co-supervised with Gaël Thomas. Cifre with Orange Labs.

- **Jean-Pierre Lozi**, *Amélioration des applications multi-fils patrimoniales par la technique du Remote Core Locking (RCL)*, University of Paris 6, July 2014, co-supervised with Gaël Thomas.

- **Suman Saha**, *Improving the Quality of Error-Handling Code in Systems Software using Function-Local Information*, University of Paris 6, March 2013, co-supervised with Julia Lawall. William Carter Award at DSN 2013.

- **Fabien Hermenier**, *Gestion dynamique des tâches dans les grappes, une approche à base de machines virtuelles*, École des Mines de Nantes, November 2009, co-supervised with Jean-Marc Menaud.

- **Nicolas Loriant**, *Évolution dynamique des systèmes d'exploitation, une approche par aspects*, École des Mines de Nantes, December 2007, co-supervised with Jean-Marc Menaud.

- **Luciano Porto Barreto**, *Conception aisée et robuste d'ordonnanceurs de processus au moyen d'un langage dédié*, Université de Rennes 1, June 2003.

- **Philippe Boinot**, *Approche déclarative à l'adaptation de systèmes de télécommunications*, Université de Rennes 1, June 2002.

- **Laurent Réveillère**, *Approche langage au développement de pilotes de périphériques robustes*, Université de Rennes 1, December 2001, co-supervised with Charles Consel.

- **Ulrik Pagh Schultz**, *Object-Oriented Software Engineering using Partial Evaluation*, Université de Rennes 1, December 2000, co-supervised with Charles Consel.

- **Eugen-Nicolae Volanschi**, *Une approche automatique à la spécialisation de programmes système*, Université de Rennes 1, February 1998, co-supervised with Charles Consel.

- **Nadine Peyrouze**, *Conception et réalisation d'un système de gestion de fichier NFS efficace et sûr de fonctionnement*, Université de Rennes 1, September 1995.

- **Bruno Rochat**, *Une approche à la construction de services fiables dans les systèmes distribués*, Université de Rennes 1, February 1992, co-supervised with Michel Banâtre.

Professional Activities

- Chair of the steering committee of the COMPAS Conference 2019-.

- Team leader of the Inria Large Scale Initiative on "Multicore virtualization for performance scaling and portability".

- **Member of the EuroSys steering committee 2013-2016.**

- **Member of the IFIP 10.4 working group on dependability.**

- Leader of the working group on "Hardware's & Operating System's Support" of the EuroTM COST Action on Transactional Memories.

- **Vice-chair of ACM/SIGOPS from July 2003 to June 2007.**

- Chair of the French chapter of ACM/SIGOPS October 2002 - March 2005.

- Treasurer of the French chapter of ACM/SIGOPS April 2000 - September 2002.

- Jury member for the award of the best EuroSys PhD thesis in Systems (Roger Needham Award), in 2007, 2008, 2009, 2011, 2014.

- **PC member of SOSP 2021**, October 2021.

- PC member of NSDI 2021.

- PC member of Usenix ATC 2020, July 2020.

- PC member of VEE 2020, March 2020.

- **PC member of SOSP 2019**, Canada, October 2019.

- Co-organizer of PLOS 2019, Canada, October 2019.

- PC member of SuperComputing 2019, USA, July 2019.

- PC member of Usenix ATC 2019, Renton, Washington, USA, July 2019.

- **PC member of OSDI 2018**, Carlsbad, California, USA, October 2018.

- **PC co-chair of DSN 2018, Luxemburg, June 2018.**

- PC member of EuroSys 2018, Porto, Portugal, April 2018.

- PC member of USENIX ATC 2017, Santa Clara, California, USA, July 2017.

- PC member of DSN 2017, Denver, Colorado, USA, June 2017.

- PC member of OPODIS 2016, Madrid, Spain, December 2016.

- PC member of EDCC 2016, Göteborg, Sweden, September 2016.

- PC member of DSN 2016, Toulouse, France, June 2016.

- PC member of APLOS 2016, Atlanta, Georgia, USA, March 2016.

- PC member of TRIOS 2015, Monterey, California, USA, October 2015.

- PC member of EDCC 2015, Paris, France, September 2015.

- Co-organizer of PLOS 2015, Monterey, California, USA, October 2015.

- PC member of DSN 2015, Rio de Janeiro, Brazil, June 2015.

- Wine chair, Sponsor co-chair, Grant co-chair of Eurosys 2015, Bordeaux, France, April 2015.

- PC member of WOSD 2014, Naples, Italy, November 2014.

- PC member of TRIOS 2014, Broomfield, Colorado, USA, October 2014.

- PC member of APSYS 2014, Beijing, China, June 2014.

- Program committee of VEE 2014, Salt Lake City, USA, March 2014.

- Program committee of IEEE CloudCom 2013, virtualization track, Bristol, UK, December 2013.

- Co-organizer of PLOS 2013, Nemacolin Woodlands Resort, Pennsylvania, USA, November 2013.

- Program committee of DSN 2013, Budapest, Hungary, June 2013.

- Program committee of EuroSys 2013, Prague, Czech Republic, April 2013.

- Program committee of ICDCN 2013, Mumbai, India, January 2013.

- Poster Chair of DSN 2012, Boston, USA, June 2012.

- **Co-chair of track on Distributed OS and Middleware of ICDCS 2012, Macau, China, June 2012.**

- Program committee of SYSTOR 2012, Haifa, Israel, June 2012.

- Poster Chair of EuroSys 2012, Bern, Switzerland, April 2012.

- Program committee of SRDS 2011, Madrid, Spain, October 2011.

- Program committee and co-organizer of PLOS 2011, Cascais, Portugal, October 2011.

- Program committee of APSys 2011, Shanghai, China, July 2011.

- Program committee of CFSE-8 (8th Conférence française sur les systèmes d'exploitation), St-Malo, May 2011.

- Program committee of SYSTOR 2011, Haifa, Israel, May 2011.

- Program committee of the 5th EuroSys Doctoral workshop, Saltzburg, April 2011.

- Program committee of HotDep'10, Vancouver, Canada, October 2010.

- Program committee of DCCS/DSN'2010 (IEEE Conference on Dependable Systems and Networks), Chicago, IL, June 2010.

- **Program chair of EuroSys 2010, Paris, April 2010.**

- Program committee of SSS'09 (International Symposium on Stabilization, Safety and Security of Distributed Systems, track multicore computing), Lyon, November 2009.

- **Program chair of PLOS 2009 (5th Workshop on Programming Languages and Operating Systems), Big Sky, MT, USA, October 2009.**

- Program committee of CFSE-7 (7th Conférence française sur les systèmes d'exploitation), Toulouse, September 2009.

- Program committee of ICWS 2009 (IEEE International Conference on Web Services), Los Angeles, July 2009.

- Program committee of ACP4IS 2009 (7th Workshop on Aspects, Components, and Pattern for Infrastructure Software), Charlottesville, Virginia, March 2009.

- Program committee of ICWS 2008 (IEEE International Conference on Web Services), Beijing, September 2008.

- Program committee of ASPLOS'2008 (ACM International Conference on Architectural Support for Programming Languages and Operating Systems), Seattle, March 2008.

- Program committee of EDCC'2008 (European Dependable Computing Conference), Kaunas, Lithuania, 2008.

- Program committee of DSN/PDS 2008 (IEEE Conference on Dependable Systems and Networks), Anchorage, Alaska, June 2008.

- Program committee of HASE'2008 (11th IEEE High Assurance Systems Engineering Symposium), Nanjing, China, December 2008.

- Program committee of CFSE-6 (6th Conférence française sur les systèmes d'exploitation), Fribourg, February 2008.

- Program committee of ICWS 2007 (IEEE International Conference on Web Services), Salt Lake City, July 2007.

- Program committee of DCCS/DSN'2007 (IEEE Conference on Dependable Systems and Networks), Edinburgh, June 2007.

- Program committee of ICDCS'2007 (IEEE International Conference on Distributed Computing Systems), Toronto, June 2007.

- Program committee of ISORC'2007 (10th IEEE International Symposium on Object and component-oriented Real-time distributed), Santorin, Greece, May 2007.

- Program committee of EuroSys 2007 (ACM Sigops/EuroSys Conference), Lisbon, March 2007.

- Program committee of PLOS 2006 (Linguistic Support for Modern Operating Systems, ASPLOS XII Workshop), San José, October 2006.

- Program committee of SRDS 2006 (25th IEEE Symposium on Reliable Distributed Systems), Leeds, October 2006.

- Program committee of DCCS/DSN'2006 (IEEE Conference on Dependable Systems and Networks), Philadelphia, June 2006.

- Program committee of CFSE-5 (5-Ieme Conférence française sur les systèmes d'exploitation), Perpignan, October 2006.

- Jury member for award of the best French thesis in operating system, October 2006 during CFSE-5.

- Program committee and Poster chair of EuroSys 2006 (ACM Sigops/EuroSys Conference), Leuven, April 2006.

- Program committee of ISORC'2006 (9th IEEE International Symposium on Object and component-oriented Real-time distributed), Gyeongju, Korea, April 2006.

- Publicity chair of Middleware 2005 (ACM/IFIP/USENIX 6th International Middleware Conference), Grenoble, November 2005.

- Program committee of SRDS 2005 (24th IEEE Symposium on Reliable Distributed Systems), Orlando, October 2005.

- Program committee of HASE 2005 (9th Intl. Symposium on High Assurance Systems Engineering), Heidelberg, October 2005.

- Program committee of SaNSO 2005 (First international workshop on System and Networking for Smart Objects), Fukuoka, July 2005.

- Program committee of ICPP-05 (International Conference on Parallel Processing), Oslo, June 2005.

- Program committee of ICDCS-2005 (International Conference on Distributed Computing Systems), Columbus (Ohio), June 2005.

- Program committee of ISAS 2005 (Second International Service Availability Symposium), Berlin, April 2005.

- Program committee of CFSE-4 (4$^{\text{ème}}$ Conférence française sur les systèmes d'exploitation), Le Croisic, April 2005.

- Program Chair of Renpar 2005, Le Croisic, April 2005.

- General Chair of Renpar/CFSE/SympaAA/JC 2005, Le Croisic, April 2005.

- Program committee of EW2004 (11th ACM European Sigops Workshop), Leuven, September 2004.

- Program committee of PLOS-2004 (ECOOP Workshop on Programming Languages and Operating Systems), Oslo, June 2004.

- Program committee of DSN'2004 (IEEE Conference on Dependable Systems and Networks), Firenze, June 2004.

- Program committee of FTDCS'2004 (International Workshop on Future Trends of Distributed Computing Systems), Suzhou (China), May 2004.

- Program committee of DAIS'2003 (4th IFIP International Conference on Distributed Applications '& Interoperable Systems), Paris, November 2003.

- Program committee of CFSE-3 (3$^{\text{ème}}$ Conférence française sur les systèmes d'exploitation), La Colle sur Loup, October 2003.

- Jury member for award of the best French thesis in operating system issued in October 2003 during CFSE-3. Award sponsored by the ASF (French chapter of the ACM/SIGOPS) and Microsoft.

- Program committee of DSN'2003 (IEEE Conference on Dependable Systems and Networks), San Francisco, June 2003.

- General Chair of the tenth ACM SIGOPS European Workshop, St-Emilion, September 2002.

- Program committee of EDCC-4 (European Dependable Computing Conference), Toulouse, October 2002.

- Program committee of SRDS'2001 (IEEE Symposium on Reliable Distributed Systems), New-Orleans, October 2001.

- Program committee of DSN'2001 (IEEE Conference on Dependable Systems and Networks), Göteborg, Sweden, July 2001.

- Program committee of CFSE-2 (2$^{\text{ème}}$ Conférence française sur les systèmes d'exploitation), Paris, April 2001.

- Jury member for the award of the best French thesis in operating system issued in April 2001 during CFSE-2. Award sponsored by the ASF (French chapter of the ACM/SIGOPS).

- Program committee of SRDS'2000 (Symposium on Reliable Distributed Systems), Nürnberg, October 200.

- Program committee of PEPM'00 (ACM SIGPLAN Workshop on Partial Evaluation and Semantics-Based Program Manipulation), Boston, December 1999.

- Program committee of SRDS'99 (Symposium on Reliable Distributed Systems), Lausanne, Switzerland, October 1999.

- Program committee of EDCC-3 (European Dependable Computing Conference), Prague, Czech Republic, September 1999.

- Program committee of EDCC-2 (European Dependable Computing Conference), Taormina, Italy, October 1996.

Bibliography

The most significant papers are printed in bold face.

Proceedings

[A1] G. Muller, M. Vieira, *Proceedings of the 48th Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN)*, 2018, Luxembourg city, Luxembourg.

[A2] C. Morin, G. Muller, *Proceedings of the 5th European conference on Computer systems (EuroSys)*, 2010, Paris, France.

[A3] E. Eide, G. Muller, O. Spinczyk, *Proceedings of the fifth workshop on programming languages and operating systems (PLOS)*, Big Sky, Montana, October 2009.

Journal Papers

[J1] G. Lena Cota, S. Ben Mokhtar, G. Gianini, E. Damiani, J. Lawall, G. Muller, L. Brunie, RACOON++: A Semi-Automatic Framework for the Selfishness-aware Design of Cooperative Systems, *IEEE Transactions on Dependable and Secure Computing*, 10.1109/TDSC.2017.2706286, Year: 2017

[J2] J-P. Lozi, F. David, G. Thomas, J. Lawall, G. Muller, Fast and Portable Locking for Multicore Architectures *ACM Transactions on Computer Systems* 33 (4): 13, 2016.

[J3] W. Maldonado, P. Marlier, P. Felber, J. Lawall, G. Muller, E. Rivière, Supporting Time-Based QoS Requirements in Software Transactional Memory, *ACM Transactions on Parallel Computing* 2(2): 10, 2015.

[J4] T.F. Bissyandé, L. Réveillère, Y.-D. Bromberg, J. Lawall, G. Muller, Implementing an embedded compiler using program transformation rules, *Software:Practice and Experience*, 10.1002/spe.2225. p. 177–196, January 2015, 45(2).

[J5] N. Palix, S. Saha, G. Thomas, C. Calvès, J. Lawall, G. Muller, Faults in Linux 2.6, *ACM Transactions On Computer Systems*. 2014, 4:1-4:40.

[J6] T. Bissyandé, L. Réveillère, J. Lawall, G. Muller, Automatic Generation of Debugging Interfaces to the Linux Kernel, *Journal of Automated Software Engineering*.

[J7] F. Hermenier, J. Lawall, G. Muller, BtrPlace: A Flexible Consolidation Manager for Highly Available Applications, *Transactions on Dependable and Secure Computing*, p. 273-286, September 2013.

[J8] J. Lawall, J. Brunel, N. Palix, R. R. Hansen, H. Stuart, G. Muller, WYSIWIB: Exploiting Fine-Grained Program Structure in a Scriptable API-Usage Protocol Finding Process, *Software:Practice and Experience*, 37(13), January 2013.

[J9] S. Legtchenko, S. Monnet, P. Sens, G. Muller, RelaxDHT: a churn-resilient replication strategy for peer-to-peer distributed hash-tables, *ACM Transactions on Autonomous and Adaptive Systems*, July 2012, 7(2).

[J10] L. Burgy, L. Réveillère, J. Lawall, G. Muller, Zebu: A Language-Based Approach for Network Protocol Message Processing, *IEEE Transactions on Software Engineering*, 37(4), p. 575-591, 2011.

[J11] P. Cointe, J. Noyé, R. Douence, T. Ledoux, J.-M. Menaud, G. Muller, M. Südholt, Programmation post-objets : des langages d'aspects aux langages de composants, *RSTI L'Objet, colloque en l'honneur de Jean-Francois Perrot 10*, 4, 2004.

[J12] G. Muller, J. Lawall, S. Thibault, R. E. V. Jensen, A Domain-Specific Language Approach to Programmable Networks, *IEEE - Transactions on Systems, Man and Cybernetics*, 2003.

[J13] D. McNamee, J. Walpole, C. Pu, C. Cowan, C. Krasic, C. Goel, C. Consel, G. Muller, and R. Marlet. Specialization tools and techniques for systematic optimization of system software. *ACM Transactions on Computer Systems*, 19, 2001, p. 217–251.

[J14] L. Clausen, U. Schultz, C. Consel, and G. Muller. Java bytecode compression for low-end embedded systems. *ACM Transactions on Programming Languages and Systems*, 22(3), 2000, p. 471–489.

[J15] S. Thibault, C. Consel, R. Marlet, G. Muller, and J. Lawall. Static and dynamic program compilation by interpreter specialization. *Higher-Order and Symbolic Computation 13*, 3, 2000, p. 161–178.

[J16] G. Muller, R. Marlet, and E.N. Volanschi. Accurate Program Analyses for Successful Specialization of Legacy System Software *Theoretical Computer Science*, 248(1–2), 2000, p. 201–210.

[J17] G. Muller and U. Schultz. Harissa: A hybrid approach to Java execution. *IEEE Software*, p. 44–51, 1999.

[J18] C. Consel, L. Hornof, J. Lawall, R. Marlet, G. Muller, J. Noyé, S. Thibault, and N. Volanschi. Tempo: Specializing systems applications and beyond. *ACM Computing Surveys, Symposium on Partial Evaluation*, 30(3), 1998.

[J19] C. Consel, L. Hornof, J. Lawall, R. Marlet, G. Muller, J. Noyé, S. Thibault, and N. Volanschi. Partial evaluation for software engineering. *ACM Computing Surveys, Symposium on Partial Evaluation*, 30(3), 1998.

[J20] G. Muller, M. Banâtre, N. Peyrouze, et B. Rochat. Lessons from FTM: an experiment in the design & implementation of a low cost fault tolerant system. *IEEE Transaction on Reliability*, June 1996. Extended version appeared in INRIA research report 2517.

[J21] M. Banâtre, P. Joubert, C. Morin, G. Muller, B. Rochat, et P. Sanchez. Stable transactional memories and fault tolerant architectures. *Operating System Review*, 25:68–72, 1991.

[J22] J. P. Banâtre, M. Banâtre, et G. Muller. Quelques aspects du système Gothic. *Techniques et Sciences Informatiques*, 6(2), 1987.

Book Chapters

[B1] G. Muller, C. Consel, et C. Pu. Specialization. In J. Urban, et P. Dasgupta, editors, *Encyclopedia of Distributed Computing*, Kluwer Academic Publisher. 1999.

[B2] G. Muller and P. Sens. Migration et points de reprise dans les systèmes distribués : techniques de mise en œuvre et mécanismes communs. In *Placement Dynamique et Répartition de Charge : application aux systèmes parallèles et répartis*, p. 57–80. INRIA, 1997.

[B3] G. Muller. Gothic : le concept de mémoire stable et son utilisation. In J. P. Banâtre, M. Banâtre, éditeurs, *Expérience dans la construction d'un système distribué: Gothic*, chapitre 7, p. 177–216. InterEditions, 1991.

Conference Papers

[C1] T. Dinh Ngoc, B. Teabe, A. Tchana, G. Muller, D. Hagimont, Mitigating vulnerability windows with hypervisor transplant, *in : EuroSys 2021*.

[C2] B. Teabe, P. Yuhala, A. Tchana, F. Hermenier, D. Hagimont, G. Muller, (No) Compromis: Paging Virtualization Is Not a Fatality, *in : VEE 2021*.

[C3] Y. Ghigoff, J. Sopena, K. Lazri, A. Blin, G. Muller, BMC: Accelerating Memcached using Safe In-kernel Caching and Pre-stack Processing, *in : NSDI 2021*.

[C4] B. Lepers, R. Gouicem, D. Carver, J.-P. Lozi, N. Palix, M.-V. Aponte, W. Zwaenepoel, J. Sopena, J. Lawall, G. Muller, Provable multicore schedulers with Ipanema: application to work conservation, *in : EuroSys 2020*.

[C5] L. Serrano, V.-A. Nguyen, F. Thung, L. Jiang, D. Lo, J. Lawall, G. Muller, SPINFER: Inferring Semantic Patches for the Linux Kernel, *in : ATC 2020*.

[C6] R. Gouicem, D. Carver, J.-P. Lozi, J. Sopena, B. Lepers, W. Zwaenepoel, N. Palix, J. Lawall, G. Muller, Fewer Cores, More Hertz: Leveraging High-Frequency Cores in the OS Scheduler for Improved Application Performance, *in : ATC 2020*.

[C7] F. Thung, S. A Haryono, L. Serrano, G. Muller, J. Lawall, D. Lo, L. Jiang, Automated Deprecated-API Usage Update for Android Apps: How Far are We?, *in : 27th IEEE International Conference on Software Analysis, Evolution and Reengineering (SANER)*.

[C8] C. Courtaud, J. Sopena, G. Muller, D. G. Perez, Improving Prediction Accuracy of Memory Interferences for Multicore Platforms, *in : RTSS 2019*.

[C9] B. Bui, D. Mvondo, B. Teabe, K. Jiokeng, L. Wapet, A. Tchana, G. Thomas, D. Hagimont, G. Muller, N. Depalma, When extended para-virtualization (XPV) meets NUMA, *in : EuroSys 2019*.

[C10] J. Lawall, G. Muller, Coccinelle: 10 Years of Automated Evolution in the Linux Kernel, *in : EuroSys 2018*.

[C11] J. Bouron, S. Chevalley, B. Lepers, W. Zwaenepoel, R. Gouicem, J. Lawall, G. Muller, J. Sopena, The Battle of the Schedulers: FreeBSD ULE vs. Linux CFS, *in : EuroSys 2018*.

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